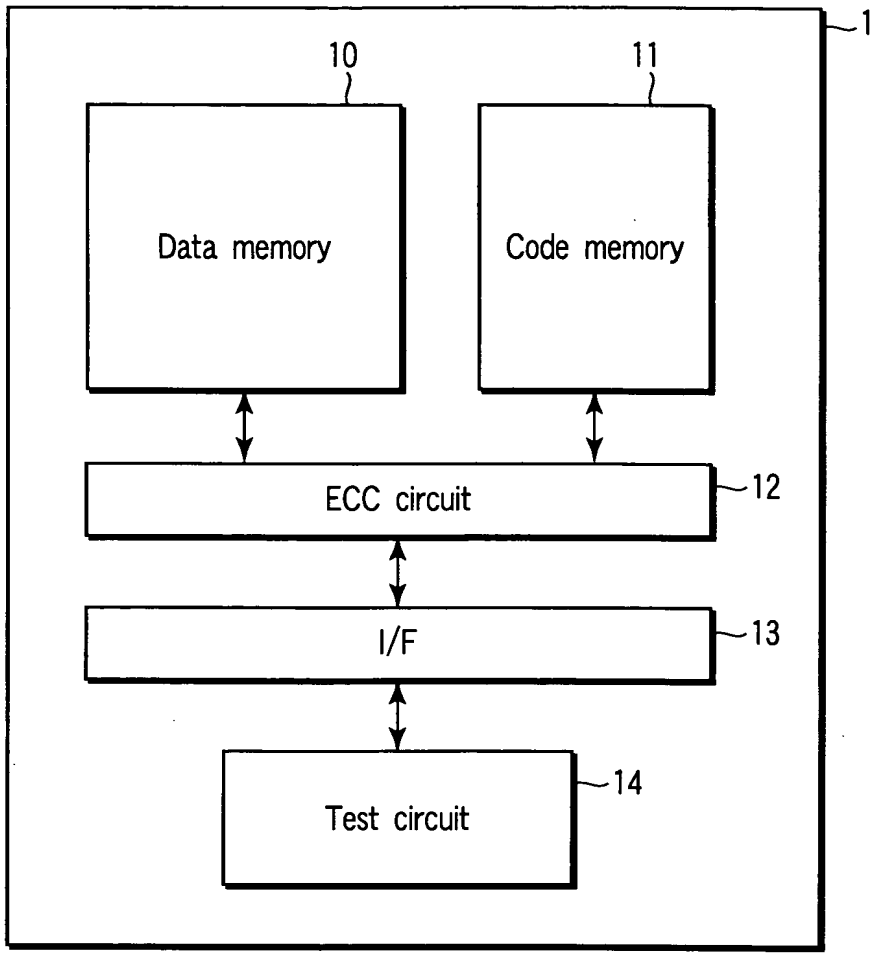

FIG. 1

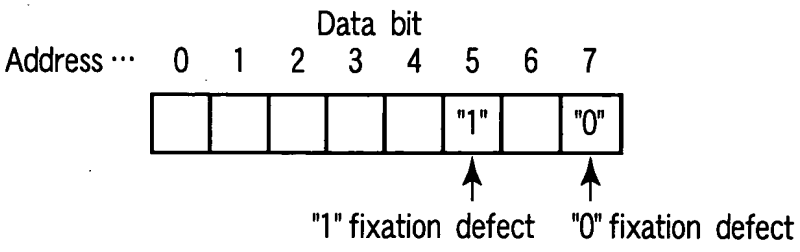

FIG. 2A

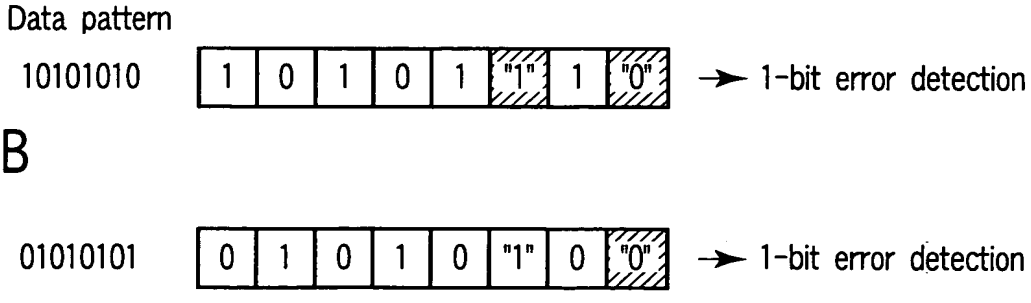

FIG. 2B

FIG. 2C

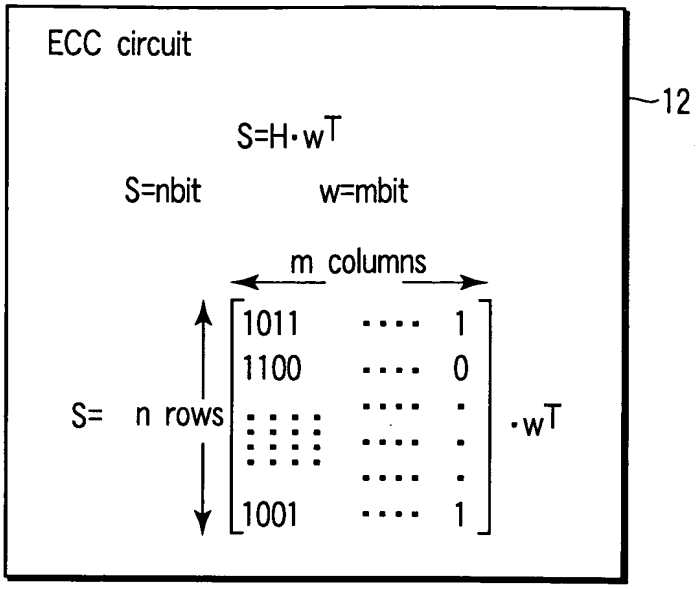

FIG. 3

Hamming martrix : H(n×m)=(8×5)

1	2	3	4	5	6	7	8
0	1	0	1	1	0	1	1
1	0	0	1	0	1	0	1
1	0	1	0	1	0	1	0
1	1	1	0	1	1	0	1
0	1	1	1	0	1	1	0

FIG. 4

	Data input : w	Code output : S	
State 1	0 0 0 0 0 0 0 0	0 0 0 0 0	← ALL "0"
State 2	0 0 0 0 0 0 1 1	0 1 1 1 1	
	0 0 0 0 1 1 1 1	1 0 0 1 0	
	0 0 1 1 1 1 1 1	0 1 1 0 0	
	0 1 1 1 1 1 1 1	1 1 1 1 1	← ALL "1"
	0 1 1 1 1 1 0 0	1 0 0 0 0	
	0 1 1 1 0 0 0 0	0 1 1 0 1	
	0 1 0 0 0 0 0 0	1 0 0 1 1	
	0 0 0 0 0 0 0 0	0 0 0 0 0	
State 1 →	0 1 0 1 0 1 0 1	1 1 0 1 1	
State 2 →	0 1 0 1 0 1 1 0	1 0 1 0 0	
	0 1 0 1 1 0 1 0	0 1 0 0 1	
	0 1 1 0 1 0 1 0	1 0 1 1 1	
	1 0 1 0 1 0 1 0	0 1 0 1 0	
	1 0 1 0 1 0 0 1	0 0 1 0 1	
	1 0 1 0 0 1 0 1	1 1 0 0 0	
	1 0 0 1 0 1 0 1	0 0 1 1 0	
	0 1 0 1 0 1 0 1	1 1 0 1 1	
	↑ 50	↑ 51	

FIG. 5

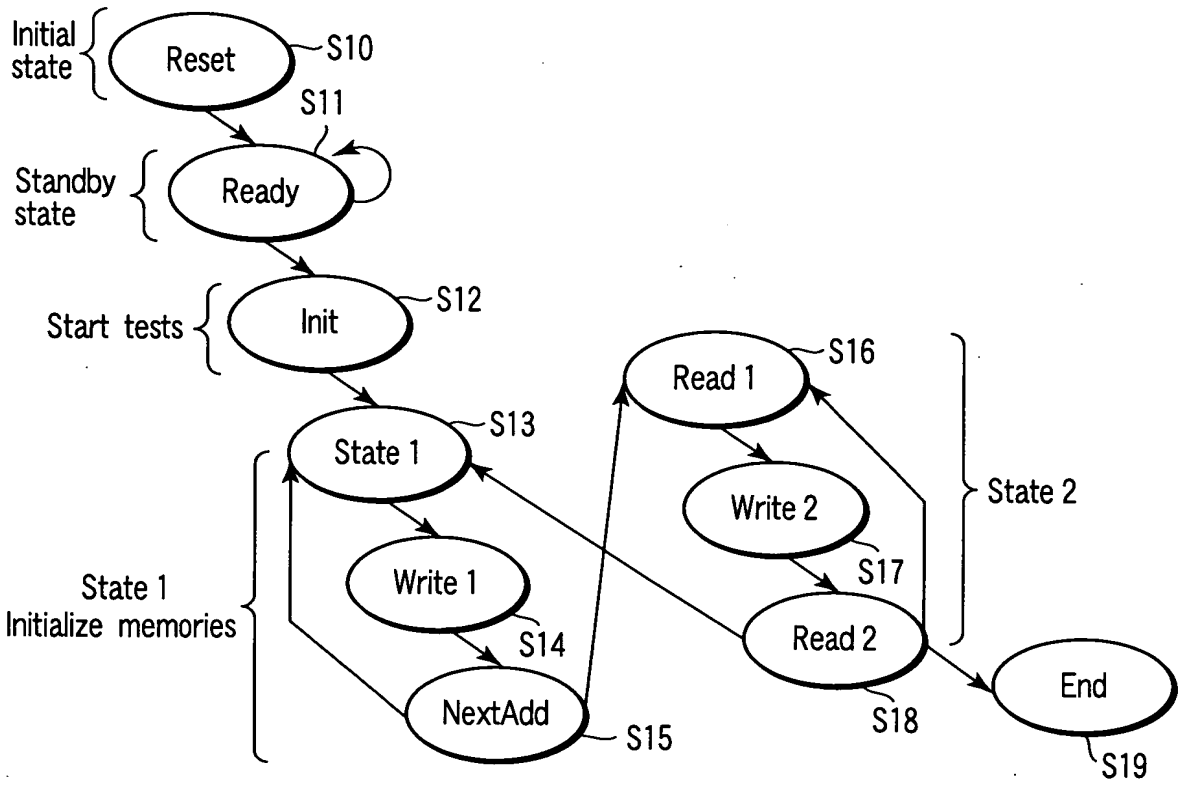
Initial state
Reset
S10
S11
Standby state
Ready
Start tests
Init
S12
S13
State 1
Write 1
S14
NextAdd
S15
State 1
Initialize memories
S16
Read 1
Write 2
S17
Read 2
S18
State 2
End
S19

FIG. 6